# **VLMTG41S2U1**





# Standard SMD LED PLCC-2



### DESCRIPTION

This device has been designed to meet the increasing demand for InGaN true green SMD LED.

The package of the VLMTG41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with clear epoxy.

## **PRODUCT GROUP AND PACKAGE DATA**

- Product group: LED
- Package: SMD PLCC-2
- · Product series: standard
- Angle of half intensity: ± 60°

### **FEATURES**

- High efficient InGaN technology
- · EIA and ICE standard package
- · Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according to JEDEC level 2a
- AEC-Q101 qualified
- · ESD-withstand voltage: Up to 2 kV HBM according to JESD22-A114-B
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · Automotive: Backlighting in dashboards and switches
- Telecommunication: Indicator and backlighting in telephone and fax
- · Backlighting for audio and video equipment
- Backlighting in office equipment
- Indoor and outdoor message boards
- Flat backlight for LCDs, switches, and symbols
- · Illumination purposes, alternative to incandescent lamps
- General use

PARTS TABLE														
PART	COLOR		IMINO TENSI (mcd)		at I <sub>F</sub> (mA)	WAV	/ELEN (nm)	GTH	at I <sub>F</sub> (mA)		ORWAN OLTAC (V)		at I <sub>F</sub> (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMTG41S2U1-GS08	True green	224	380	560	10	515	530	541	10	-	3.2	4.2	20	InGaN on Sapphire
VLMTG41S2U1-GS18	True green	224	380	560	10	515	530	541	10	-	3.2	4.2	20	InGaN on Sapphire

ABSOLUTE MAXIMUM RAT VLMTG41	<b>FINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwis	e specified)		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
DC forward current	$T_{amb} \le 80 \ ^{\circ}C$	١ <sub>F</sub>	20	mA
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.1	A
Power dissipation		Pv	84	mW
Junction temperature		Tj	110	°C
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C
Thermal resistance junction/ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	360	K/W

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COMPLIANT HALOGEN

FREE

**GREEN** 

(5-2008)



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OPTICAL AND ELECTR VLMTG41, TRUE GRE		<b>ICS</b> (T <sub>amb</sub> = 25 °	C, unless	otherwis	e specifi	ed)			
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Luminous intensity	I <sub>F</sub> = 10 mA	VLMTG41S2U1	Ι <sub>V</sub>	224	380	560	mcd		
Dominant wavelength	I <sub>F</sub> = 10 mA		λ <sub>d</sub>	515	530	541	nm		
Peak wavelength	I <sub>F</sub> = 10 mA		λρ	-	520	-	nm		
Angle of half intensity	I <sub>F</sub> = 10 mA		φ	-	± 60	-	deg		
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>	-	3.2	4.2	V		
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 10 mA		TC <sub>VF</sub>	-	- 4	-	mV/K		
Temperature coefficient of $I_V$	I <sub>F</sub> = 10 mA		TCIV	-	- 0.25	-	%/K		

#### Note

Not designed for reverse operation

LUMINOUS	JS INTENSITY CLASSIFICATION				
GROUP	LIG	IT INTENSITY (I	mcd)		
STANDARD	OPTIONAL	MIN.	MAX.		
S	2	224	280		
т	1	280	355		
I	2	355	450		
U	1	450	560		

#### Note

 Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

These type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups are not be orderable.

# COLOR CLASSIFICATION

	TRUE GREEN DOM. WAVELENGTH (nm)				
GROUP					
	MIN.	MAX.			
3	515	523			
4	521	529			
5	527	535			
6	533	541			

Note

· Wavelengths are tested at a current pulse duration of 25 ms.

CROSSING TABLE	
VISHAY	OSRAM
VLMTG41S2U1	LTT67CS2U1

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

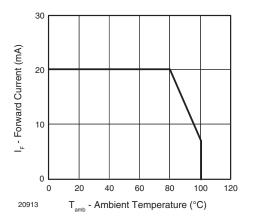
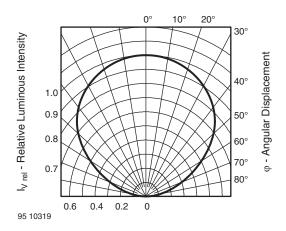


Fig. 1 - Forward Current vs. Ambient Temperature





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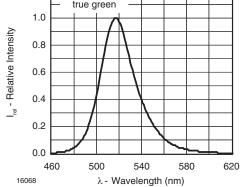


Fig. 3 - Relative Luminous Intensity vs. Wavelength

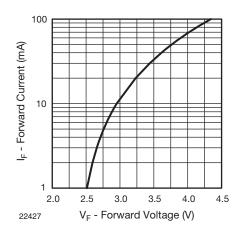


Fig. 4 - Forward Current vs. Forward Voltage

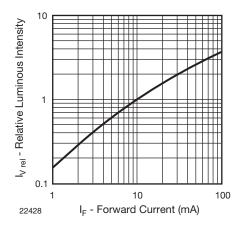


Fig. 5 - Relative Luminous Intensity vs. Forward Current

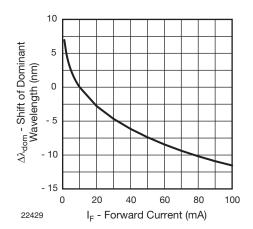


Fig. 6 - Shift of Dominant Wavelength vs. Forward Current

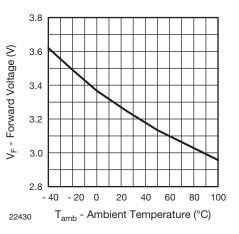


Fig. 7 - Forward Voltage vs. Ambient Temperature

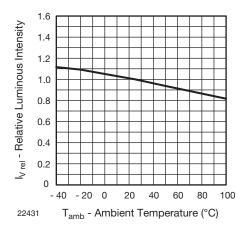


Fig. 8 - Relative Luminous Intensity vs. Ambient Temperature

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3 For technical questions, contact: <u>LED@vishay.com</u>



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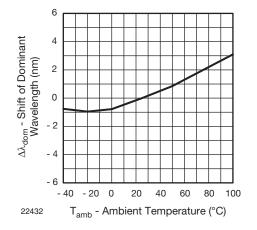
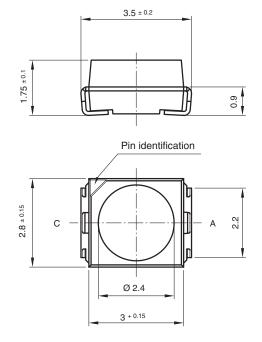
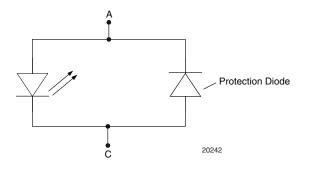


Fig. 9 - Shift of Dominant Wavelength vs. Ambient Temperature

## **PACKAGE DIMENSIONS** in millimeters



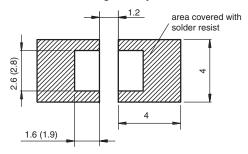




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technical drawings according to DIN specifications

**Mounting Pad Layout** 



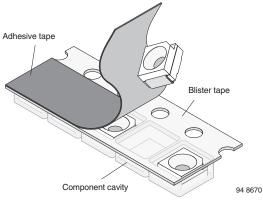




# METHOD OF TAPING/POLARITY AND TAPE AND REEL

# SMD LED (VLM.3..., VLM.4...-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



# TAPING OF VLM.3..., VLM.4...

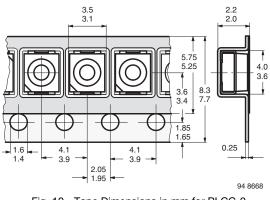


Fig. 10 - Tape Dimensions in mm for PLCC-2

### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

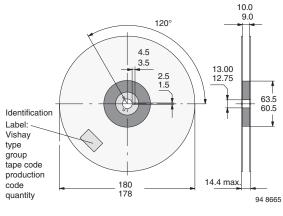
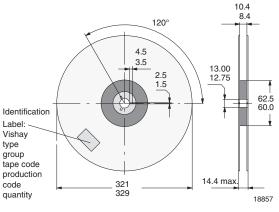


Fig. 11 - Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED





## SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

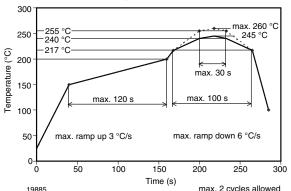


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

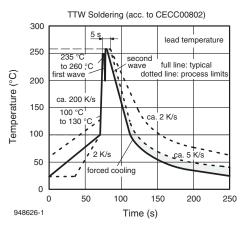


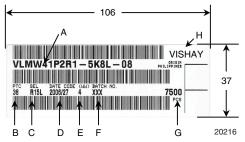
Fig. 14 - Double Wave Soldering of Opto Devices (all Packages)

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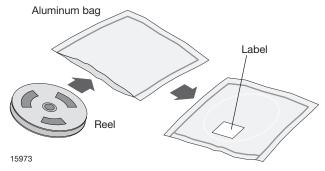
# BAR CODE PRODUCT LABEL (example)



- A. Type of component
- B. Manufacturing plant
- C. SEL selection code (bin): e.g.: R1 = code for luminous intensity group 5L = code for chrom. coordinate group
- D. Date code year/week
- E. Day code (e.g. 4: Thursday)
- F. Batch no.
- G. Total quantity
- H. Company code

### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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### **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity  $\leq$  60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen)

or

96 h at 60  $^{\circ}\text{C}$  + 5  $^{\circ}\text{C}$  and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

T	CAUTION his bag contains E –SENSITIVE DEVICES	<b>2a</b>
1. Shelf life in sealed bag 12 month	ns at <40°C and < 90% relativ	e humidity (RH)
<ol> <li>After this bag is opened devices vapor-phase reflow, or equivalet 260°C) must be:         <ol> <li>Mounted within 672 hou</li> <li>Stored at ≤10% RH.</li> </ol> </li> </ol>	nt processing (peak package l	body temp.
<ol> <li>Devices require baking before m         <ol> <li>Humidity Indicator Card</li> <li>2a or 2b is not met.</li> </ol> </li> </ol>	ounting if: is >10% when read at 23°C ±	±5°C or
<ol> <li>If baking is required, devices ma 192 hours at 40°C + 5°C/-0' 96 hours at 60±5°Cand &lt;5% 24 hours at 100±5°C</li> </ol>	°C and <5%RH (dry air/nitro	ntainers or
Bag Seal Date:(If blank, see	e bar code label)	
Note: LEVEL defined by	EIA JEDEC Standard JESD	22-A113

Example of JESD22-A112 level 2a label

# ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

#### VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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